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Judith Stoker

## IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Applicant:

John Glenn Edelen et al.

Paper No.:

Serial No.:

10/655,363

Group Art Unit: 2861

Filed:

September 4, 2003

Examiner: Lamson D. Nguyen

For:

N-Well and Other Implanted Temperature Sense Resistors in Inkjet Print Head Chips

## **INFORMATION DISCLOSURE STATEMENT**

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98 Applicant herewith submits a certain patent reference which the Patent & Trademark Office may wish to consider in examining the above-identified application. The reference is listed on the attached Form PTO-1449. This reference appears to be the patent referenced in the previous Office Actions. As this reference was not officially cited on PTO Form-892, or by Applicants, it is therefore being cited in this submission.

It is believed no copy of this U.S. patent reference is required. No representation is made or intended that a prior art search has been made or that no better art than that listed is available.

Please charge any fees required, or credit any overpayment, to Deposit Account No. 04-1133.

Respectfully submitted,

Bv.

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FORM PTO - 1449 LIST OF PATENTS AND COMMITTIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT								DS: 21043-157 APPLICANT: al. FILING DATE:	T: 2001-0886.01;  John Glenn Edelen et  September 4, 2003  nd Other Implanted  nse Resistors in  nd Chips	SERIAL NO.: 10/655,363 GROUP: 2861		
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.